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<u></u>			Application No.	09/540,613	
TRANSMITTAL FORM			Filing Date	March 31, 2000	
to be used for all correspondence after initial filing)			First Named Inventor	Carl M. Ellison	
o pe used for all correspondence differences		Art Unit	2134		
· ·			Examiner Name	Ellen C. Tran	
	mu O badasi		Attorney Docket Number	42390P8628	
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	ENCLOS	SURES (che	ck all that apply)	After Allowance Communication	
Fee Transmittal Forn	1	Drawing(s	3)	to Group	
Fee Attached		Licensing	-related Papers	Appeal Communication to Board of Appeals and Interferences	
Amendment / Respo	nse	Petition		Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)	
After Final		Petition to	o Convert a al Application	Proprietary Information	
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Individual name	BLAKELY	, SOKOLO	FY, TAYLOR & ZA	TLIMITATA DET	
Signature					
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sufficient postage as i	is correspondence irst class mail in a	is being deposi n envelope addr	ted with the United States ressed to: Commissioner fo	Postal Service on the date shown below with or Patents, P.O. Box 1450, Alexandria, VA	
22313-1450. Typed or printed na		A. Morrow		7 1 2004	
Signature	Kiis	tion a	man (wir) 96/04/2004.	Date September 21, 2004	

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Effective 01/01/2004. Patent fees are subject to annual revision.

Applicant claims small entity status. See 37 CFR 1.27.

Complete if Known 09/540,613 Application Number March 31, 2000 Filing Date Carl M. Ellison First Named Inventor Ellen C. Tran **Examiner Name** 2134 42390P8628 Art Unit Attorney Docket No.

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SUBMITTED BY	11	DT	Regist	ration	No.	39,	926	Telephone	(7	14) 557-3800
Name (Print/Type) Gregory D. Caldwell	41		(Attorne	y/Agent)		1		5.4		09/21/04
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Carl M. Ellison et al.

Examiner:

Ellen C. Tran

Application No.: 09/540,613

Art Unit:

2134

Docket No.: P8628

Phone No.: (703) 305-8917

Filed: 03/31/00

For: MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97

COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Enclosed please find an Information Disclosure Statement, which is being submitted after the mailing of the first Office Action but before the mailing of a Final Office Action. A copy of each cited reference is also enclosed.

In accordance with 37 C.F.R. § 1.97(e), I hereby state that no item of information in the Information Disclosure Statement filed herewith was cited in a communication from a foreign patent office in a counterpart foreign application, and, to my knowledge after making reasonable inquiry, no item of information contained in this Information Disclosure Statement was known to any individual designated in 37 CFR 1.56(c) more than 3 months prior to the filing of this Information Disclosure Statement.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject

application, and is not to be construed as an admission that the information cited in this statement is material to patentability.

Applicants respectfully request that the Examiner consider the cited reference(s), initial each reference on the enclosed Information Disclosure Statement to indicate such consideration, and return a copy of the initialed Information Disclosure Statement to Applicants. If the Examiner has any questions, the Examiner is invited to contact the undersigned at (512) 732-3927.

Respectfully submitted,

Dated: 9/2014

Michael R. Barré
Patent Attorney
Intel Americas, Inc.
Registration No. 44,023
(512) 732-3927

c/o Blakely, Sokoloff, Taylor & Zafman, LLP 12400 Wilshire Blvd. Seventh Floor Los Angeles, CA 90025-1026

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number	09/540,613			
Filing Date	March 31, 2000			
First Named Inventor	Carl M. Ellison			
Art Unit	2134			
Examiner Name	Ellen C. Tran			
Attorney Docket Number	42390P8628			

			U.S. PATE	NT DOCUMENTS		
		Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages or Relevant	
	Cite No.'	Number - Kind Code ² (if known)	or Issue Date MM-DD-YYYY	Applicant of Cited Document	Figures Appear	
	11	US-5,950,221		Draves et al.		
	2	US-5,898,883	04-27-1999			
	3	US-5,668,971	09-16-1997	Neufeld		
	4	US-5,522,075		Robinson et al.		
	15	US-5,303,378	04-12-1994	Cohen		
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'Applicant's unique citation designation number (optional). See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. Ehter Office that issued the document, by the two-letter code (WIPO Standard ST.3). For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible policant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wir) 08/11/2003.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Carl M. Ellison et al.

Examiner:

Ellen C. Tran

Application No.: 09/540,613

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2134

Docket No.: P8628

Phone No.: (703) 305-8917

Filed: 03/31/00

For: MANAGING A SECURE

ENVIRONMENT USING A CHIPSET IN ISOLATED EXECUTION MODE

CROSS REFERENCE TO RELATED APPLICATIONS

COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Applicants hereby cross-reference the following related patents and patent applications, which are all assigned to the same assignee as the present patent application.

> I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313 on:

> > Date of Deposit

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Reference	Application	_	
No.	Serial No.	Patent No.	
I	09/672,603		Controlling Accesses To Isolated Memory Using A Memor
- -			Controller For Isolated Execution
2	09/822,986	1	Checking File Integrity Using Signature Generated In
3	00/555 054		Isolated Execution
3	09/538,951	}	Platform And Method For Issuing And Certifying A
4	00/500 0 44		Hardware-Protected Attestation Key
4	09/539,344	ļ	Managing A Secure Platform Using A Hierarchical
5	00///0 505	-	Executive Architecture In Isolated Execution Mode
3	09/668,585		Managing A Secure Platform Using A Hierarchical
6	09/541,108	 	Executive Architecture In Isolated Execution Mode
			Platform And Method For Remote Attestation Of A Platforn
7	09/540,612		Platform And Method For Generating And Utilizing A
0	00151001		Protected Audit Log
8	09/540,946		Protecting Software Environment In Isolated Execution
9	09/668,610		Protecting Software Environment In Isolated Execution
10	09/538,954		
11	09/539,348	6,760,441	Generating A Key History by Early Solated Execution
	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	0,700,141	Generating A Key Hierarchy For Use In An Isolated Execution Environment
12	09/541,477	6,507,904	
13	09/540,611		Isolated Instructions For Isolated Execution
14	09/540,613		Managing Accesses In A Processor For Isolated Execution
•	03/340,013		Managing A Secure Environment Using A Chipset In
15	09/668,408		Isolated Execution Mode
~~	05/000,400		Managing A Secure Environment Using A Chipset In
16	09/541,667		Isolated Execution Mode
17	09/672,602		Attestation Key Memory Device And Bus
			Attestation Key Memory Device And Bus
18	09/618,738	6,678,825	Controlling Access To Multiple Isolated Memories In An
10	001010100		Isolated Execution Environment
19	09/618,489	6,633,963	Controlling Access To Multiple Memory Zones In An
20	10/405 5 15		Isolated Execution Environment
20	10/683,542		Controlling Access To Multiple Memory Zones In An
- 21	00/610 555		Isolated Execution Environment
21	09/618,659	ł	Resetting A Processor In An Isolated Execution
- 22	00/5/1 5-5		Environment
22	09/751,586		Resetting A Processor In An Isolated Execution
	- <u> </u>		Environment

It may be noted that the above list includes the present application, and the above list partially overlaps the cross-reference to related applications that was filed earlier.

If there are any questions that may be cleared up in a telephone interview, the Examiner is invited to call the undersigned at (512) 732-3927.

Respectfully submitted,

Dated: 9/20/04

Michael R. Barré
Patent Attorney
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c/o Blakely, Sokoloff, Taylor & Zafman, LLP 12400 Wilshire Blvd. Seventh Floor Los Angeles, CA 90025-1026